Computational Physics with GPUs

Lecture 4: Simulating spin models II

Martin Weigel

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Slides and exercises

Check out the lecture notes and example code at

http://users.complexity-coventry.org/~weigel/GPU/

Any questions? Contact me at

Martin.Weigel@mail.com

Outline

Random number generators

2 Local updates (again)

Generalized ensembles

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 - ullet typically produce uniform distribution on $[0, \mathrm{NMAX}]$ or [0, 1]
 - further distributions (such as Gaussian) generated from transformations
- generally two types of pseudo RNGs considered
 - for general purposes, including simulations
 - or for cryptographic purposes, requiring sufficient randomness to prevent efficient stochastic inference

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PHYSICAL REVIEW LETTERS

7 DECEMBER 1992

Monte Carlo Simulations: Hidden Errors from "Good" Random Number Generators

Alan M. Ferrenberg and D. P. Landau

Center for Simulational Physics, The University of Georgia, Athens, Georgia 30602

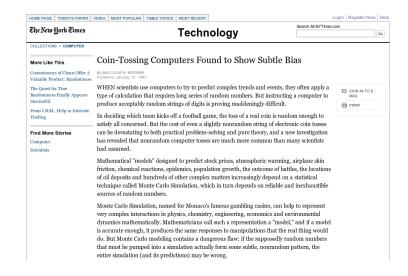
Y. Joanna Wong

IBM Corporation, Supercomputing Systems, Kingston, New York 12401 (Received 29 July 1992)

The Wolff algorithm is now accepted as the best cluster-flipping Monte Carlo algorithm for beating "critical slowing down." We show how this method can yield *incorrect* answers due to subtle correlations in "high quality" random number generators.

PACS numbers: 75.40.Mg, 05.70.Jk, 64.60.Fr

The explosive growth in the use of Monte Carlo simulations in diverse areas of physics has prompted extensive investigation of new methods and of the reliability of both old and new techniques. Monte Carlo simulations are ing model, to study the time correlations, but so far there has been no careful study of the accuracy of the thermodynamic properties which are extracted from the configurations generated by this process.



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spin models II

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 - comparison to combinatorial identities
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Test batteries:

- DieHard (1995) by G. Marsaglia, now outdated
- TestU01 (2002/2009) by P. L'Ecuyer and co-workers, quasi standard

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 - (c) setup of independent generators of the same class of RNGs using different lags, multipliers, shifts etc.

$$x_{n+1} = ax_n + c \pmod{m}.$$

Simplest choice satisfying these requirements is linear congruential generator (LCG):

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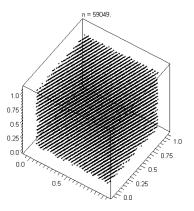
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• can be improved by choosing $m=2^{64}$ and truncation to 32 most significant bits, period $p=m\approx 10^{18}$ and 8 bytes per thread

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LCG implementation

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#define A32 1664525
#define C32 1013904223
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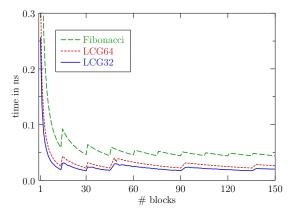
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#define MULT32 2.328306437080797e-10f

#define CONVERT(x) (MULT32*((unsigned int)(x)))
//#define CONVERT(x) _curand_uniform(x)
//#define CONVERT(x) __fdividef(__uint2float_rz(x),(float)0x100000000);
```

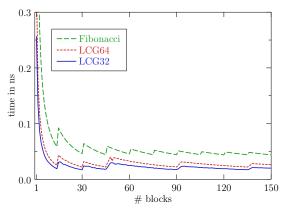
LCG: performance

How well do they perform?



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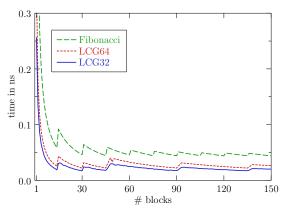
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Characteristic zig-zag pattern due to commensurability (or not) of block number of with number of multiprocessors.

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Peak performance at 58×10^9 (LCG32) and 46×10^9 (LCG64) random numbers per second, respectively.

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- checkerboard update uses random numbers in different way than sequential update
- linear congruential generators can skip ahead: "right" way uses non-overlapping sub-sequences
- "wrong" way uses sequences from random initial seeds, many of which must overlap

RNG quality: Ising results

Table: Internal energy e per spin and specific heat C_V for a 1024×1024 Ising model with periodic boundary conditions at $\beta=0.4$.

method	e	$\Delta_{ m rel}$	C_V	$\Delta_{ m rel}$	$t_{\rm up}^{k=1}$	$t_{\rm up}^{k=100}$					
exact	1.106079207	0	0.8616983594		•						
sequential update (CPU)											
LCG32	1.1060788(15)	-0.26	0.83286(45)	-63.45							
LCG64	1.1060801(17)	0.49	0.86102(60)	-1.14							
Fibonacci, $r = 512$	1.1060789(17)	-0.18	0.86132(59)	-0.64							
checkerboard update (GPU)											
LCG32	1.0944121(14)	-8259.05	0.80316(48)	-121.05	0.2221	0.0402					
LCG32, random	1.1060775(18)	-0.97	0.86175(56)	0.09	0.2221	0.0402					
LCG64	1.1061058(19)	13.72	0.86179(67)	0.14	0.2311	0.0471					
LCG64, random	1.1060803(18)	0.62	0.86215(63)	0.71	0.2311	0.0471					
MWC, same a	1.1060800(18)	0.45	0.86161(60)	-0.15	0.2293	0.0435					
MWC, different a	1.1060797(18)	0.28	0.86168(62)	-0.03	0.2336	0.0438					
Fibonacci, $r = 521$	1.1060890(15)	6.43	0.86099(66)	-1.09	0.2601	0.0661					
Fibonacci, $r = 1279$	1.1060800(19)	0.40	0.86084(53)	-1.64	0.2904	0.0700					
XORWOW (cuRAND)	1.1060654(15)	-9.13	0.86167(65)	0.04	0.7956	0.0576					
XORShift/Weyl	1.1060788(18)	-0.23	0.86184(53)	0.27	0.2613	0.0721					
Philox4x32_7	1.1060778(18)	-0.79	0.86109(65)	-0.93	0.2399	0.0523					
Philox4x32_10	1.1060777(17)	-0.85	0.86188(61)	0.30	0.2577	0.0622					

Use these LCG generators for the previously developed simulation code for the 2D Ising model. Exact results are available for comparison. Test case of 1024×1024 system at $\beta=0.4$, 10^7 sweeps.

- checkerboard update uses random numbers in different way than sequential update
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RNG quality: TestU01 results

Table: The memory footprint is measured in bits per thread. For the TestU01 results, if (too many) failures in SmallCrush are found, Crush and BigCrush are not attempted; likewise with failures in Crush. The performance column shows the peak number of 32-bit uniform floating-point random numbers produced per second on a fully loaded GTX 480 device.

generator	bits/thread	failur	es in Tes	Ising test	perf.	
		SmallCrush	h Crush	BigCrush		$ imes 10^9/{ m s}$
LCG32	32	12	_	_	failed	58
LCG32, random	32	3	14		passed	58
LCG64	64	None	6	_	failed	46
LCG64, random	64	None	2	8	passed	46
MWC	64 + 32	1	29	_	passed	44
Fibonacci, $r = 521$	≥ 80	None	2	_	failed	23
Fibonacci, $r = 1279$	≥ 80	None	(1)	2	passed	23
XORWOW (cuRAND)	192	None	None	1/3	failed	19
MTGP (cuRAND)	≥ 44	None	2	2	_	18
XORShift/Weyl	32	None	None	None	passed	18
Philox4x32_7	(128)	None	None	None	passed	41
Philox4x32_10	(128)	None	None	None	passed	30

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General conclusion: fast, but not good enough

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Comprehensive discussion in

M. Mansen, M. Weigel, and A. K. Hartmann, Eur. Phys. J. Special Topics 210, 53 (2012.)

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Generalized ensembles

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GPU code v3 - kernel

__global__ void metro_checkerboard_three(spin_t *s, int *ranvec, int offset)
{
   int n = blockDim.x*blockIdx.x + threadIdx.x;
   int cur = blockDim.x*blockIdx.x + threadIdx.x + offset*(N/2);
   int north = cur + (1-2*offset)*(N/2);
   int east = ((north+1)%L) ? north + 1 : north-L+1;
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int ide = s[cur]*(s[west]+s[north]+s[east]+s[south]);
   if(fabs(RAN(ranvec[n])*4.656612e-10f) < tex1Dfetch(boltzT, ide+2*DIM)) {
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⇒ local changes can be tracked

Ising model: Measurements (cont'd)

energy changes __global__ void metro_checkerboard_three(spin_t *s, int *ranvec, int offset) { int n = blockDim.x*blockIdx.x + threadIdx.x; ... int south = (n - (1-2*offset)*L + N/2)%(N/2) + (1-offset)*(N/2); int ide = s[cur]*(s[west]+s[north]+s[east]+s[south]); int ie = 0; if(fabs(RAN(ranvec[n])*4.656612e-10f) < tex1Dfetch(boltzT, ide+2*DIM)) { s[cur] = -s[cur]; ie -= 2*ide; }</pre>

Ising model: Measurements (cont'd)

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__global__ void metro_checkerboard_three(spin_t *s, int *ranvec, int offset)
{
   int n = blockDim.x*blockIdx.x + threadIdx.x;
   ...
   int south = (n - (1-2*offset)*L + N/2)%(N/2) + (1-offset)*(N/2);

   int ide = s[cur]*(s[west]+s[north]+s[east]+s[south]);
   int ie = 0;
   if(fabs(RAN(ranvec[n])*4.656612e-10f) < tex1Dfetch(boltzT, ide+2*DIM)) {
        s[cur] = -s[cur];
        ie -= 2*ide;
   }
}</pre>
```

```
butterfly sum
```

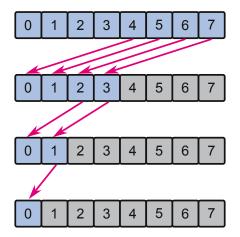
```
__shared__ int deltaE[THREADS];
deltaE[n] = ie;

for(int stride = THREADS>>1; stride > 0; stride >>= 1) {
    __syncthreads();
    if(n < stride) deltaE[n] += deltaE[n+stride];
}

if(n == 0) result[blockIdx.y*GRIDL+blockIdx.x] += deltaE[0];
}</pre>
```

Ising model: Measurements (cont'd)

Access pattern for reduction:



Ising Spin glass

Recall Hamiltonian:

$$\mathcal{H} = -\sum_{\langle i,j
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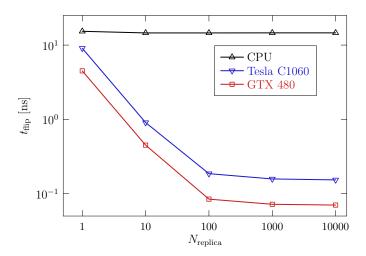
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- same domain decomposition (checkerboard)
- slightly bigger effort due to non-constant couplings
- higher performance due to larger independence?
- very simple to combine with parallel tempering

Spin glass: performance



Spin glasses: continued

Seems to work well with

- 15 ns per spin flip on CPU
- 70 ps per spin flip on GPU

but not better than ferromagnetic Ising model.

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Further improvement: use multi-spin coding

- Synchronous multi-spin coding: different spins in a single configurations in one word
- Asynchronous multi-spin coding: spins from different realizations in one word

Implementation

```
for(int i = 0; i < SWEEPS_LOCAL; ++i) {
 float r = RAN(ranvecS[n]);
 if(r < boltzD[4]) sS(x1,y) = ~sS(x1,y);
 else {
   p1 = JSx(x1m,y) ^ sS(x1,y) ^ sS(x1m,y); p2 = JSx(x1,y) ^ sS(x1,y) ^ sS(x1p,y);
   p3 = JSy(x1,ym) ^ sS(x1,y) ^ sS(x1,ym); p4 = JSy(x1,y) ^ sS(x1,y) ^ sS(x1,yp);
   if(r < boltzD[2]) {
     ido = p1 | p2 | p3 | p4;
     sS(x1,v) = ido ^sS(x1,v):
   } else {
     ido1 = p1 & p2; ido2 = p1 ^ p2;
     ido3 = p3 & p4; ido4 = p3 ^ p4;
     ido = ido1 | ido3 | (ido2 & ido4):
     sS(x1,y) = ido ^sS(x1,y);
 }
 __syncthreads();
 r = RAN(ranvecS[n]):
 if(r < boltzD[4]) sS(x2,v) = ~sS(x2,v);
 else {
   p1 = JSx(x2m,y) ^ sS(x2,y) ^ sS(x2m,y); p2 = JSx(x2,y) ^ sS(x2,y) ^ sS(x2p,y);
   p3 = JSy(x2,ym) ^ sS(x2,y) ^ sS(x2,ym); p4 = JSy(x2,y) ^ sS(x2,y) ^ sS(x2,yp);
   if(r < boltzD[2]) {
     ido = p1 | p2 | p3 | p4;
     sS(x2,y) = ido ^sS(x2,y);
   } else {
     ido1 = p1 & p2; ido2 = p1 ^ p2;
     ido3 = p3 & p4; ido4 = p3 ^ p4;
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- Asynchronous multi-spin coding: spins from different realizations in one word
- \Rightarrow brings us down to about 2 ps per spin flip

Janus

 ${\sf JANUS}, \ {\sf a} \ {\sf modular} \ {\sf massively} \ {\sf parallel} \ {\sf and} \ {\sf reconfigurable} \ {\sf FPGA-based} \ {\sf computing} \ {\sf system}.$





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		JANUS		PC		
MODEL	Algorithm	Max size	perfs	AMSC	SMSC	NO MSC
3D Ising EA	Metropolis	96^{3}	16 ps	45×	190×	
3D Ising EA	Heat Bath	96^{3}	16 ps	60×		
Q = 4 3D Glassy Potts	Metropolis	16^{3}	64 ps	$1250 \times$	1900×	
Q = 4 3D disordered Potts	Metropolis	88 ³	32 ps	$125 \times$		1800×
$Q=4, C_m=4$ random graph	Metropolis	24000	2.5 ns	$2.4 \times$		10×

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Costs:

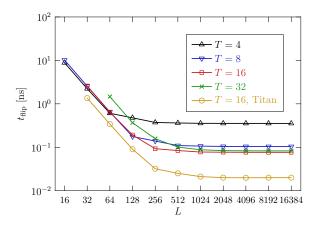
- Janus: 256 units, total cost about 700,000 Euros
- Same performance with GPU: 64 PCs (2000 Euros) with 2 GTX 295 cards (500 Euros) $\Rightarrow 200,000$ Euros
- Same performance with CPU only (assuming a speedup of ~ 50): 800 blade servers with two dual Quadcore sub-units (3500 Euros) $\Rightarrow 2,800,000$ Euros

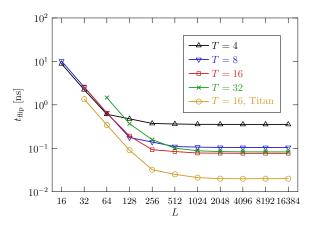
Outline

Random number generators

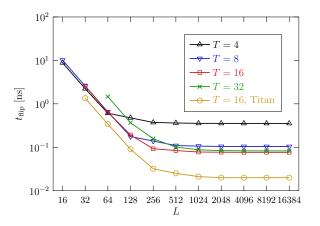
2 Local updates (again)

Generalized ensembles





For sufficiently large lattices, one achieves spin-flip times as low as 20 ps, about 250 times faster than a single CPU core.



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The number of threads is limited by the number of spins.

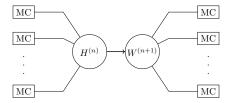
Parallel multicanonical simulations

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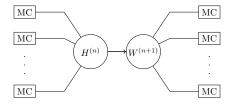
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Parallel multicanonical simulations (Zierenberg et al., 2013)



In practise, each walker is represented by a single thread in a grid.

Parallel muca (cont'd)

Each walker samples its own histogram, all of them are combined for the next weight update,

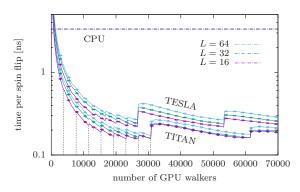
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Parallel muca (cont'd)

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This scheme can be efficiently implemented on MPI clusters (Zierenberg et al., 2013) and on GPUs.



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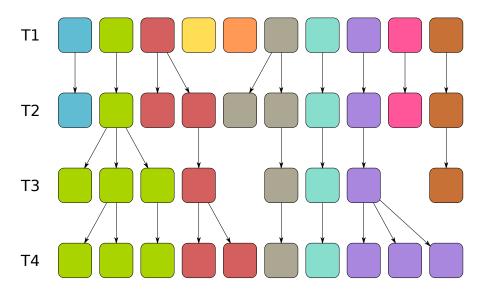
```
atomicAdd(d_histogram + E, 1);
```

How does one best go about in combining the histograms $H^{(n)}(E)$? (At least) three solutions come to mind

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```
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```

(Atomic operations guarantee the absence of data races, where the result of an operation depends on whether another parallel thread accesses the data in between read and write operations.)



Population annealing algorithm (Hukushima + Iba, 2003; Machta, 2010):

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- ② To create an approximately equilibrated sample at $\beta_i > \beta_{i-1}$, resample configurations with their relative Boltzmann weight $\exp[-(\beta_i \beta_{i-1})E_j]/Q$, where $Q = \sum \exp(-(\beta_i \beta_{i-1})E_j)$.

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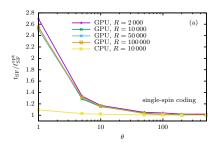
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- ① Update each copy (replica) by θ rounds of an MCMC algorithm at inverse temperature β_i .
- Calculate estimates for observable quantities $\mathcal O$ as population averages $\sum_i \mathcal O_j/R$.
- Goto step until target temperature is reached.

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Massively parallel approach

The approach is naturally suitable for an implementation on massively parallel hardware such as GPUs.





L. Barash, MW, M. Borovský, W, Janke, and L. Shchur, Comput. Phys. Commun. 220, 341 (2017).

Code at github.com/LevBarash/PAising.

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	CPU	GPU				
		SS	SC	MSC		
L	$t_{ m SF}$ [ns]	$t_{ m SF}$ [ns]	speedup	$t_{ m SF}$ [ns]	speedup	
16	23.1	0.092	251	0.0096	2406	
32	22.9	0.094	243	0.0095	2410	
64	22.6	0.095	238	0.0098	2306	
128	22.6	0.098	230	0.0098	2306	
256	22.5	0.099	227	0.0098	2295	

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Consider total work of parallel implementation. For MCMC we have

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The parallel speedup is hence

$$S = \frac{T_1}{T_p} = \left\{ \begin{array}{ccc} \frac{E+T}{E+T/p} & \stackrel{p \to \infty}{\longrightarrow} & 1 + \frac{T}{E} & \text{MCMC}, \\ p & \stackrel{p \to \infty}{\longrightarrow} & \infty & \text{PA} \end{array} \right.$$

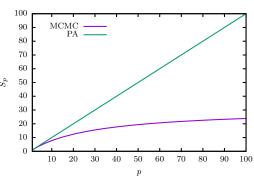
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Benchmark results for various models considered:

			CPU	C1060	GTX 480	
System	Algorithm	L	ns/flip	ns/flip	ns/flip	speed-up
2D Ising	Metropolis	32	8.3	2.58	1.60	3/5
2D Ising	Metropolis	16 384	8.0	0.077	0.034	103/235
2D Ising	Metropolis, $k = 1$	16 384	8.0	0.292	0.133	28/60
3D Ising	Metropolis	512	14.0	0.13	0.067	107/209
2D Heisenberg	Metro. double	4096	183.7	4.66	1.94	39/95
2D Heisenberg	Metro. single	4096	183.2	0.74	0.50	248/366
2D Heisenberg	Metro. fast math	4096	183.2	0.30	0.18	611/1018
2D spin glass	Metropolis	32	14.6	0.15	0.070	97/209
2D spin glass	Metro. multi-spin	32	0.18	0.0075	0.0023	24/78
2D Ising	Swendsen-Wang	10240	77.4	_	2.97	-/26
2D Ising	multicanonical	64	42.1	_	0.33	-/128
2D Ising	Wang-Landau	64	43.6	_	0.94	-/46

Summary and outlook

This lecture

In this session we considered a number of advanced features of Monte Carlo simulations on GPU, including the choice and implementation of suitable parallel random number generators, the implementation of measurement routines using parallel reductions, as well as generalized-ensemble simulations such as the multicanonical and population annealing methods that replace parallelism via domain decomposition by parallel simulations of system copies.

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Reading

- M. Manssen, M. Weigel, and A. K. Hartmann, Eur. Phys. J. Special Topics 210, 53 (2012).
- L. Yu. Barash, M. Weigel, M. Borovský, W. Janke, and L. N. Shchur, Comput. Phys. Commun. 220, 341 (2017).
- J. Gross, J. Zierenberg, M. Weigel, and W. Janke, Comput. Phys. Commun. 224, 387 (2018).

Exercises

Ising and Heisenberg models:

- Add some of the improvements discussed in this lecture. In most cases, this
 also requires changes to the driver code (execution configuration, memory
 layout etc.), not just insertion of the kernel code.
- Compare timings for different kernel versions and different block sizes etc.
- Change the code for simulations of the Heisenberg model. Check the stability.
- Add the necessary statements for measuring energies and magnetizations. Use parallel reductions.

Histograms: check the code sample histogram.tgz and write the corresponding kernel(s) to implement a code that creates a histogram out of a sequence of events.